# **BINARY DECIMAL CONVERTER**

By Bernie Mesa

This instrument has been designed as a teaching tool for both students of mathematics and electronics. In mathematics, it demonstrates the principles of the binary numerical system and its correspondence to the decimal system. In electronics, it illustrates aspects of design and construction of digital electronic circuits.

The binary numerical system consists of only two digits, zero and one. Page 6 shows some binary numbers and their decimal equivalents. Just as the decimal system is a place value system based on the powers of 10, the binary system is based on the powers of 2. The powers of 2 from 0 to 10 are highlighted. The binary system is used at the fundamental level



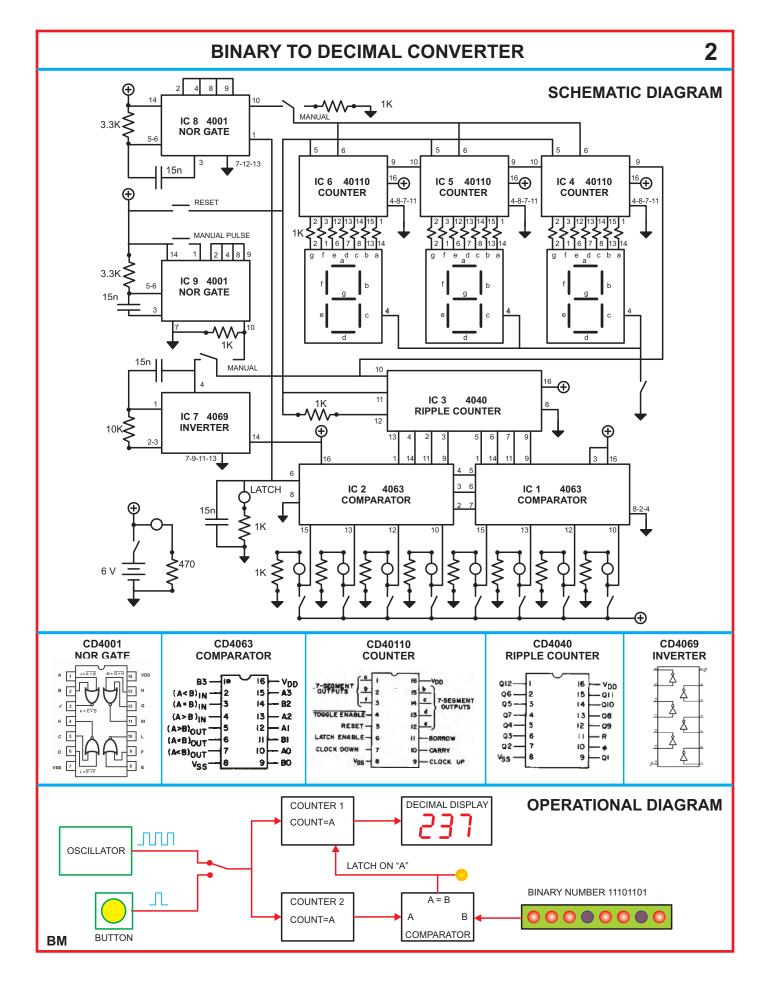
of digital circuits and computers whose basic components are electronic switches. Switches have only two positions on or off, which are interpreted as one or zero. All the billions of instructions and numbers handled by a computer executing a program are streams of zeros and ones. Binary "bits" control everything digital, from the letter pressed on the keyboard to the colors of the screen to worldwide internet transmissions.

This converter has a row of 8 switches with 8 LED's (Light Emitting Diode) as the binary input. A three digit display shows the decimal equivalent. On the automatic mode the display continually shows the correct decimal output. On the manual mode, after pushing the reset button, the decimal display shows the count of pulses from the input button. When the count matches the binary input, the yellow LED lights on.

Page 3 shows the complete electronic schematic. The circuit consists of two simultaneous pulse counters. The first counter (IC3) feeds the binary count through 8 lines to two comparators (IC1 and IC2). The second counter is composed of three IC's (4, 5, and 6) which drive the 3 decimal displays but only with the count latched by a signal from the comparators. The latching signal is activated when the comparators detect a match between the binary input and the feed from the first counter.

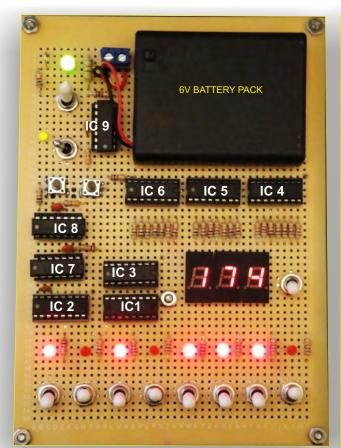
Automatic pulses originate at an oscillator (IC7). The oscillator frequency is about 4.5 kHz (4,500 pulses per second). The system resets the count to zero every 256 pulses. This happens 17 times per second shown by the blinking yellow LED. On manual mode the reset and pulse are done by pushing the respective buttons.

Pages 4 and 5 show the schematic for a very simple converter limited to four binary bits and one decimal. There are no simple IC's to convert more than four bits because most digital electronic circuits operate sequentially, the bigger converter is an example of a sequential circuit.

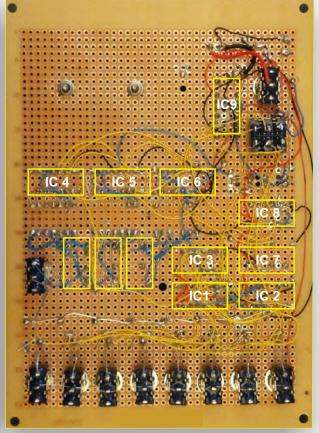


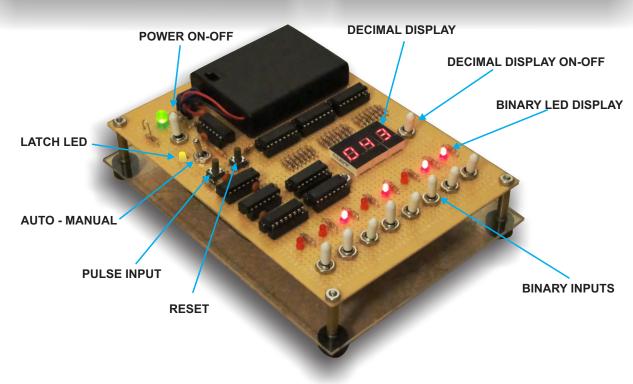
#### **BINARY TO DECIMAL CONVERTER**

#### INTRUMENT CONSTRUCTION

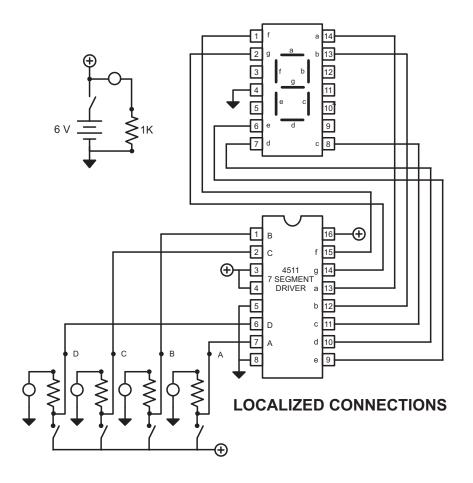


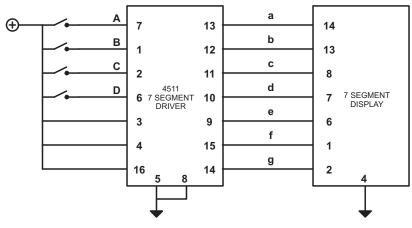
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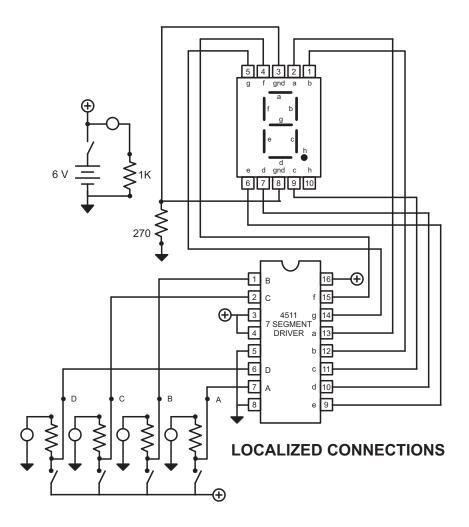
### **SINGLE DECIMAL DIGIT CIRCUIT (1)**

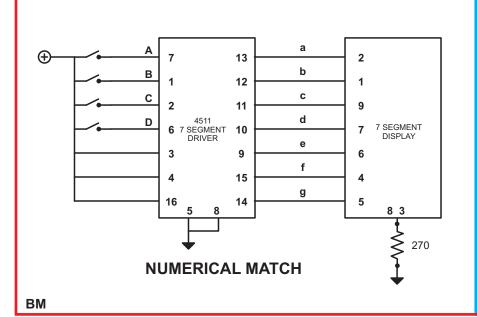


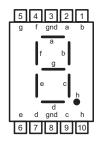


**NUMERICAL MATCH** 

#### **SINGLE DECIMAL DIGIT CIRCUIT (2)**

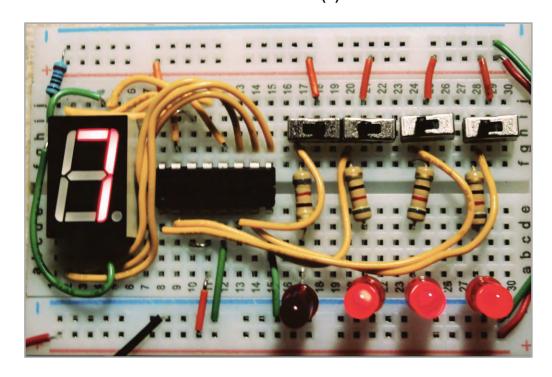


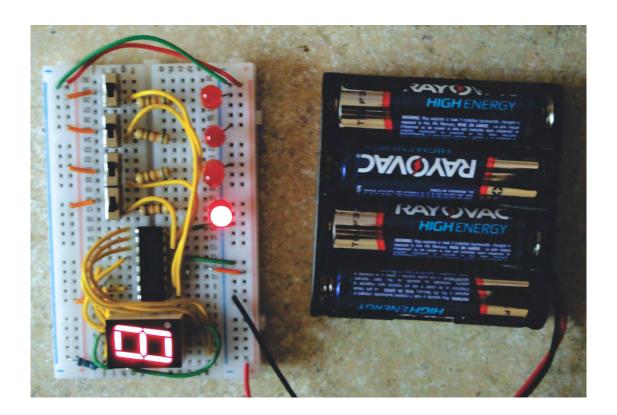




**DISPLAY CHIP 2** 

## SINGLE DECIMAL DIGIT CIRCUIT (2) BUILT ON A BREAD BOARD





BINARY - DECIMAL CONVERSION EXERCISES				8
POWERS OF 2	BINARY DIGITAL DISPLAY	BINARY NUMBERS	DECIMAL NUMBERS	
	00000000000	10	2	
	00000000000	11010	26	Si Si
	00000000000	0	0	EXAMPLES
	•0•00••00•	10100111001	1337	EX
<b>2</b> <sup>5</sup>	000000000	100000	32	
	00000000000		425	
	00000000000		11	
	00000000000		999	<u>}</u>
	00000000000		15	BINARY
	00000000000		<b>3</b> 4	0 B
	00000000000		7	ERT .
<b>2</b> <sup>7</sup>	00000000000		128	CONVERT TO
	00000000000		2000	Ü
	00000000000		255	
<b>2</b> <sup>8</sup>	00000000000		256	
	000000000	11		
	000000000	11001110		
		11111011100		CONVERT TO DECIMAL
	0 • 0 0 • • • 0 0 0 •	1001110001		0 10 10
	000000000	100001		ERT
	000000000000	1101111		ON C
	0000000000	110001		ŭ
	000000000	10010000		
	0000000000			YOUR EXAMPLES
	0000000000			KAMI
	0000000000			지 (
ВМ	00000000000			YOL